

LESSON PLAN

Name	Smt Indu	
Discipline	Computer Engineering	
Semester	4 th	
Subject	Computer Organisation	
Lesson Plan Duration	15 weeks	
Week	Theory	
	Day	Topic
1st	1st	A brief over view of the subject "Computer organization" and relevance of the studying the subject in Diploma level Program.
	2nd	CPU Organization : Concept of Registers and General Register Organization
	3rd	Concept of Stack Organization
2nd	1st	Concept of Instruction Format and types of instructions, Three, Two, One , Zero Address
	2nd	Addressing modes: Immediate, register, direct, in direct
	3rd	Addressing modes: relative, indexed.
3rd	1st	Concept of CPU Design
	2nd	Concept of Micro programmed controlled
	3rd	Concept of Hard wired controlled
4th	1st	Class Test of CPU Design
	2nd	Concept of Reduced instruction Set Computer
	3rd	Concept of Complex instruction Set Computer
5th	1st	CISC Characteristics, RICS Characteristics
	2nd	Comparison of RISC & CISC
	3rd	Seminar on Topics , Instruction formats and Addressing modes , CISC, RISC
6th	1st	Concept of Memory Organization, Memory types
	2nd	Memory Hierarchy
	3rd	ROM and RAM Chips, Concept of Memory Address Map
7th	1st	Connections of Memory Chips with the CPU
	2nd	Concept and usage of Auxiliary Memories and types
	3rd	Study of Magnetic Disks
8th	1st	Study of Magnetic Tapes
	2nd	Associative and Cache memory
	3rd	Concept of Virtual Memory

9th	1st	Concept of Memory Management
	2nd	Memory Management Hardware.
	3rd	Revision of Associative, Cache , Virtual memory
10th	1st	Read and Write operation of memory
	2nd	Concept of Input/output Organization
	3rd	Basic Input out put System BIOS and its Function
11th	1st	Testing and Initialization by BIOS, Configuring the System
	2nd	Concept of Data transfer in Computer System
	3rd	Different modes of Data Transfer : Programmed and DMA
12th	1st	Programmed I/O : Synchronous, asynchronous
	2nd	Interrupt initiated I/O
	3rd	DMA data transfer
13th	1st	Class Test of I/O Organisation
	2nd	Concept of Multi Processor Systems
	3rd	Different forms of Parallel Processing
14th	1st	Different forms of Parallel Processing ...continued
	2nd	Concept of Parallel processing and Pipe Lines
	3rd	Basic Characteristics of Multiprocessor, General purpose multiprocessors
15th	1st	Concept of Interconnection Networks and Time Shared Common Bus
	2nd	Concept of Multiport Memory, Cross Bar Switch
	3rd	Multistage Switching networks and hyper cube structures